

This card provides two eight-channel TDIF I/O interfaces with 96 kHz , $88.2 \mathrm{kHz}, 48 \mathrm{kHz}$, or 44.1 kHz operation with wordclock sync outputs on BNC connectors. Inputs and outputs are provided on standard 25-pin D-type connectors (female).
In $96 / 88.2 \mathrm{kHz}$ operation, the number of channels is limited to eight, i.e. four per I/O.

TDIF inputs/outputs
Current consumption (3.3 V) (5 V)
Operating temperature
according to TDIF specifications
5 mA
0.1 A
$0-40{ }^{\circ} \mathrm{C}$


## Jumper

In 96 kHz mode the card handles a total of 8 channels ( 4 per interface). In order to avoid different numbers of channels when switching from 96 kHz to 48 kHz and vice versa, it is possible to restrict the card to 8 channels even in 48 kHz mode. In such a case only the first interface (TDIF IN/OUT CH $\mathbf{1 - 8}$ ) is active, as shown in the table below.

| Jumper Setting | Channels on Backplane | Interface 1 | Interface 2 |
| :---: | :---: | :---: | :---: |
| 16-Ch Mode (factory default) | 16 in, 16 out | 48 kHz : Ch 1-8 | 48 kHz : Ch 9-16 |
|  |  | 96 kHz: Ch 1-4 | 96 kHz: Ch 5-8 |
| 8-Ch Mode | 8 in, 8 out | 48 kHz : Ch 1-8 | 48 kHz : unused |
|  |  | 96 kHz: Ch 1-4 | 96 kHz: Ch 5-8 |



Connector Pin Assignment, 48 kHz Operation (2 $\times 25$ pin D-type, female, UNC 4-40 thread)

|  |  | Pin | Signal CH 1-8 | Signal CH 9-16 | Pin | Signal CH 1-8 | Signal CH 9-16 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\bigcirc$ | 1 | CH 1/2 out | CH 9/10 out | 14 | GND | GND |
|  | O | 2 | CH 3/4 out | CH 11/12 out | 15 | GND | GND |
| 25 | $\because 13$ | 3 | CH 5/6 out | CH 13/14 out | 16 | GND | GND |
|  |  | 4 | CH $7 / 8$ out | CH 15/16 out | 17 | GND | GND |
|  |  | 5 | LRCK out | LRCK out | 18 | EMPH out | EMPH out |
| Solder/Crimp View | $\because$ | 6 | FS 0 out | FS 0 out | 19 | FS1 out | FS1 out |
| (or Socket View) | $\because \bigcirc$ | 7 | GND | GND | 20 | FS0 in | FS0 in |
|  | $\because:$ | 8 | FS 1 in | FS 1 in | 21 | EMPH in | EMPH in |
|  | $\because \bigcirc$ | 9 | LRCK in | LRCK in | 22 | GND | GND |
| 14 |  | 10 | $\mathrm{CH} 7 / 8$ in | CH 15/16 in | 23 | GND | GND |
|  | $\bigcirc$ | 11 | CH 5/6 in | CH 13/14 in | 24 | GND | GND |
|  | O | 12 | CH $3 / 4$ in | CH 11/12 in | 25 | GND | GND |
|  |  | 13 | CH 1/2 in | CH 9/10 in |  |  |  |

Connector Pin Assignment, $96 \mathbf{k H z}$ Operation (2 $\times 25$ pin D-type, female, UNC 4-40 thread)

|  |  | Pin | Signal CH 1-8 | Signal CH 9-16 | Pin | Signal CH 1-8 | Signal CH 9-16 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\bigcirc$ | 1 | CH 1 out | CH 5 out | 14 | GND | GND |
|  | $\bigcirc$ | 2 | CH 2 out | CH 6 out | 15 | GND | GND |
| 25 |  | 3 | CH 3 out | CH 7 out | 16 | GND | GND |
|  | $\because \bigcirc$ | 4 | CH 4 out | CH 8 out | 17 | GND | GND |
|  | $\because:$ | 5 | LRCK out | LRCK out | 18 | EMPH out | EMPH out |
| Solder/Crimp View | $\because$ | 6 | FS 0 out | FS 0 out | 19 | FS1 out | FS1 out |
| (or Socket View) | $\because:$ | 7 | GND | GND | 20 | FS0 in | FS0 in |
|  | $\because:$ | 8 | FS 1 in | FS 1 in | 21 | EMPH in | EMPH in |
|  | $\because \bullet$ | 9 | LRCK in | LRCK in | 22 | GND | GND |
| 14 | $\bullet 1$ | 10 | CH 4 in | CH 8 in | 23 | GND | GND |
|  | $\bigcirc$ | 11 | CH 3 in | CH 7 in | 24 | GND | GND |
|  | O | 12 | CH 2 in | CH 6 in | 25 | GND | GND |
|  |  | 13 | CH 1 in | CH 5 in |  |  |  |

